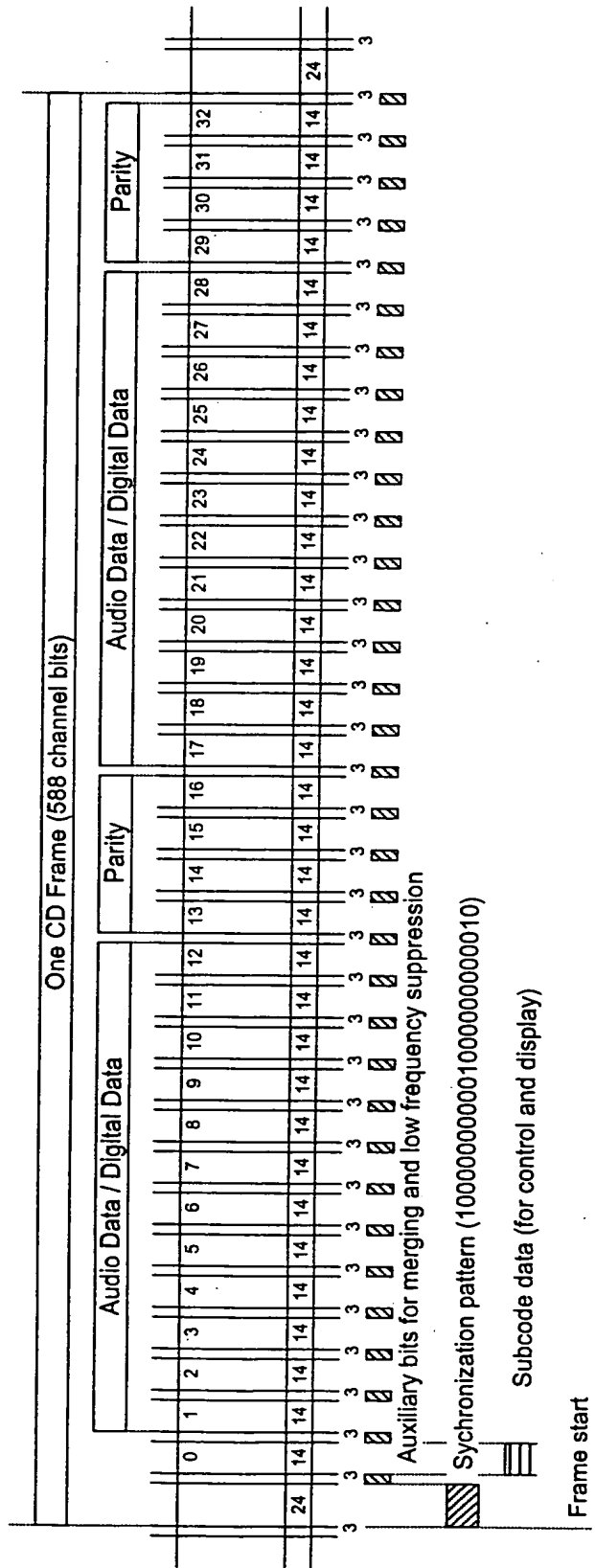


Fig. 1



70h|3 merging bits|01h|3 merging bits|70h|3 merging bits|01h|3 merging bits

10000000100010000100000000100100000000100010000100001000000000100



D9h|3 merging bits|7Ah|3 merging bits|D9h|3 merging bits|7Ah|3 merging bits

100000000100010010000000001000010000000001000100010010000000010000



Fig. 2

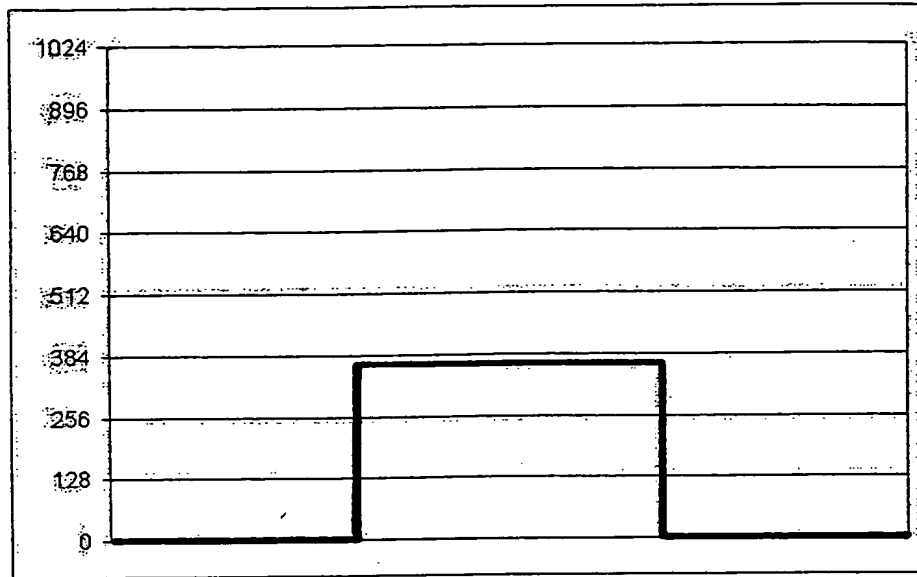


Fig. 3a

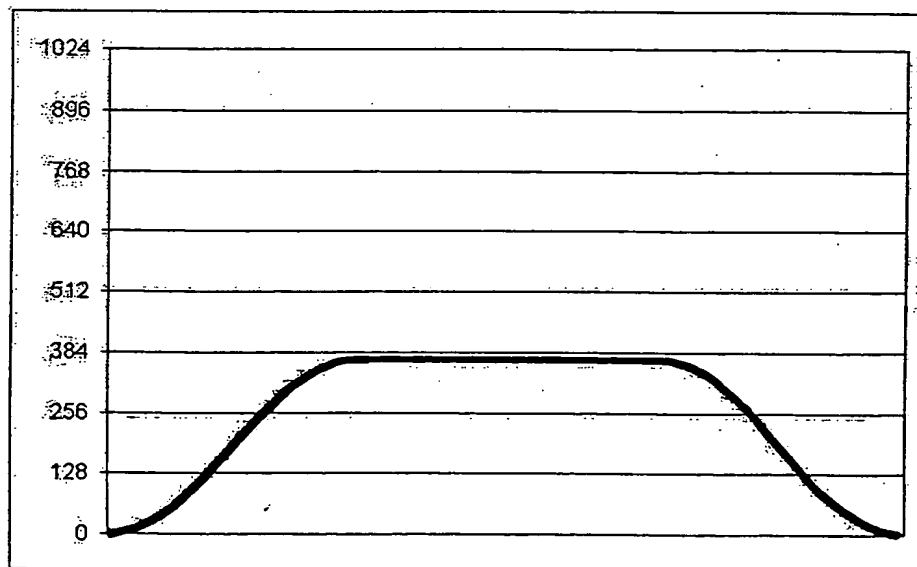


Fig. 3b

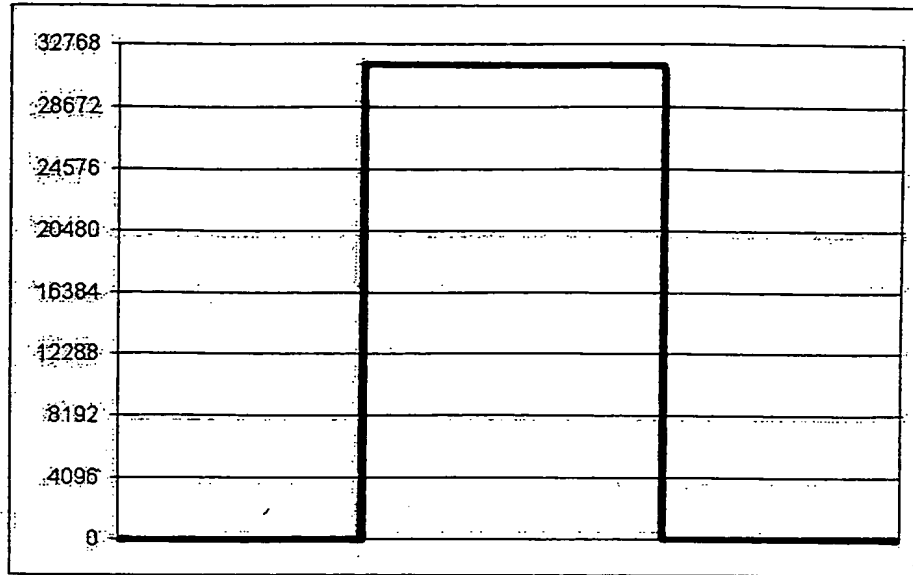


Fig. 4a

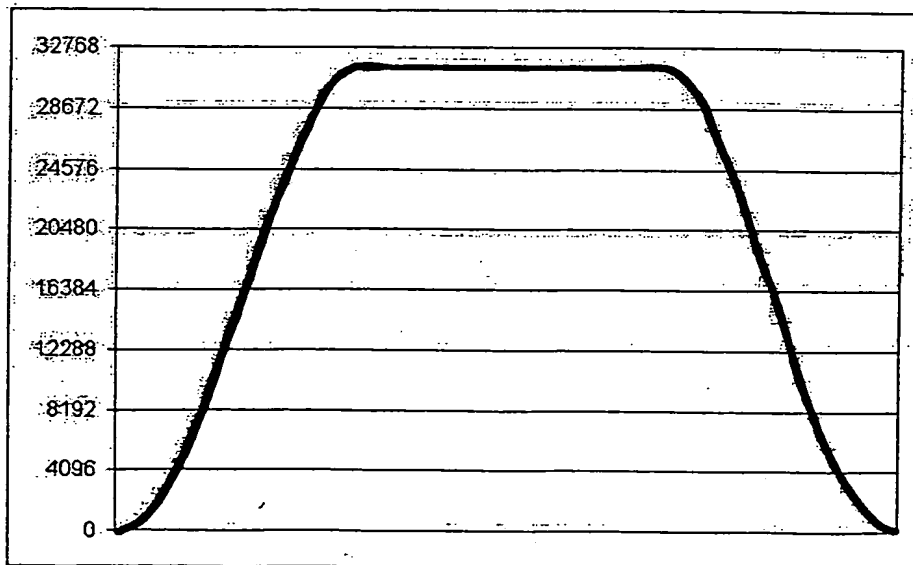
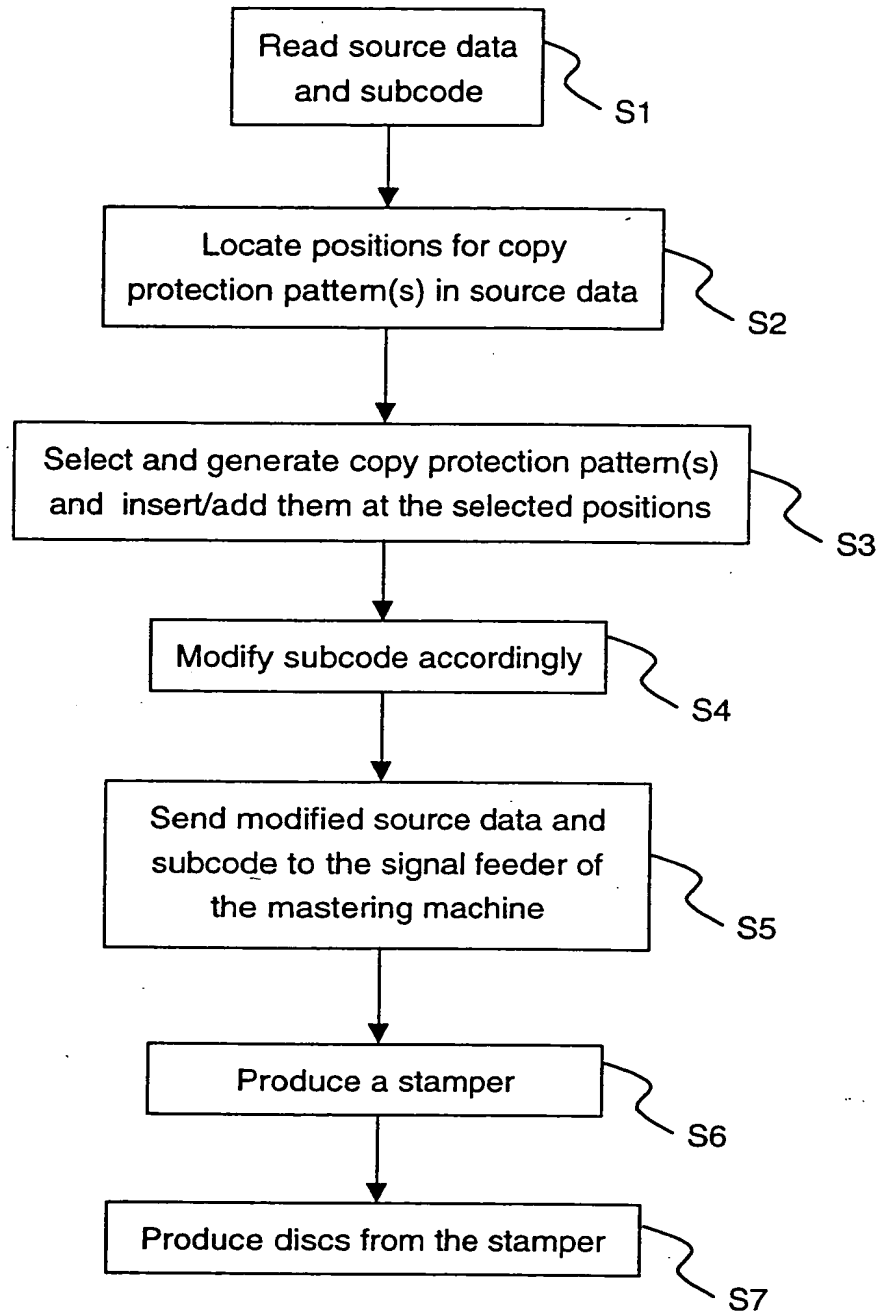
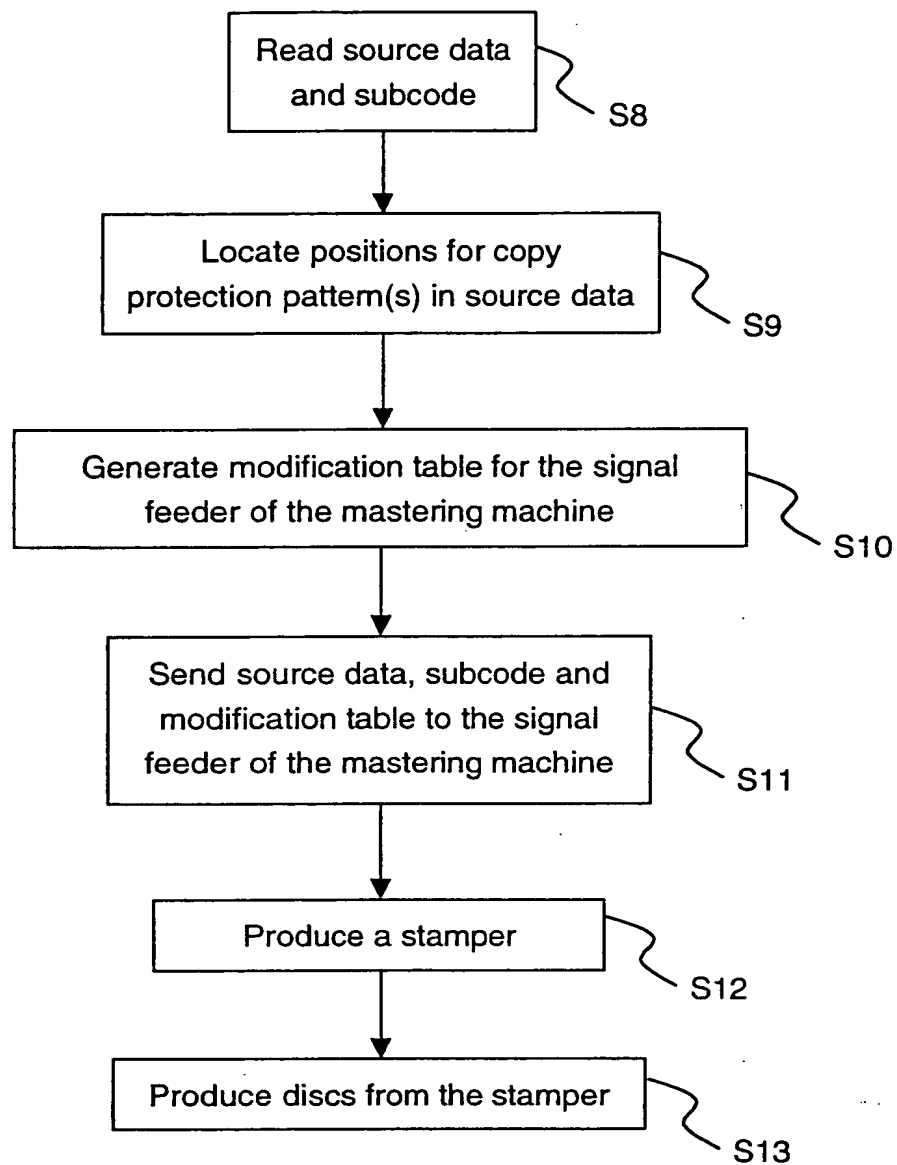
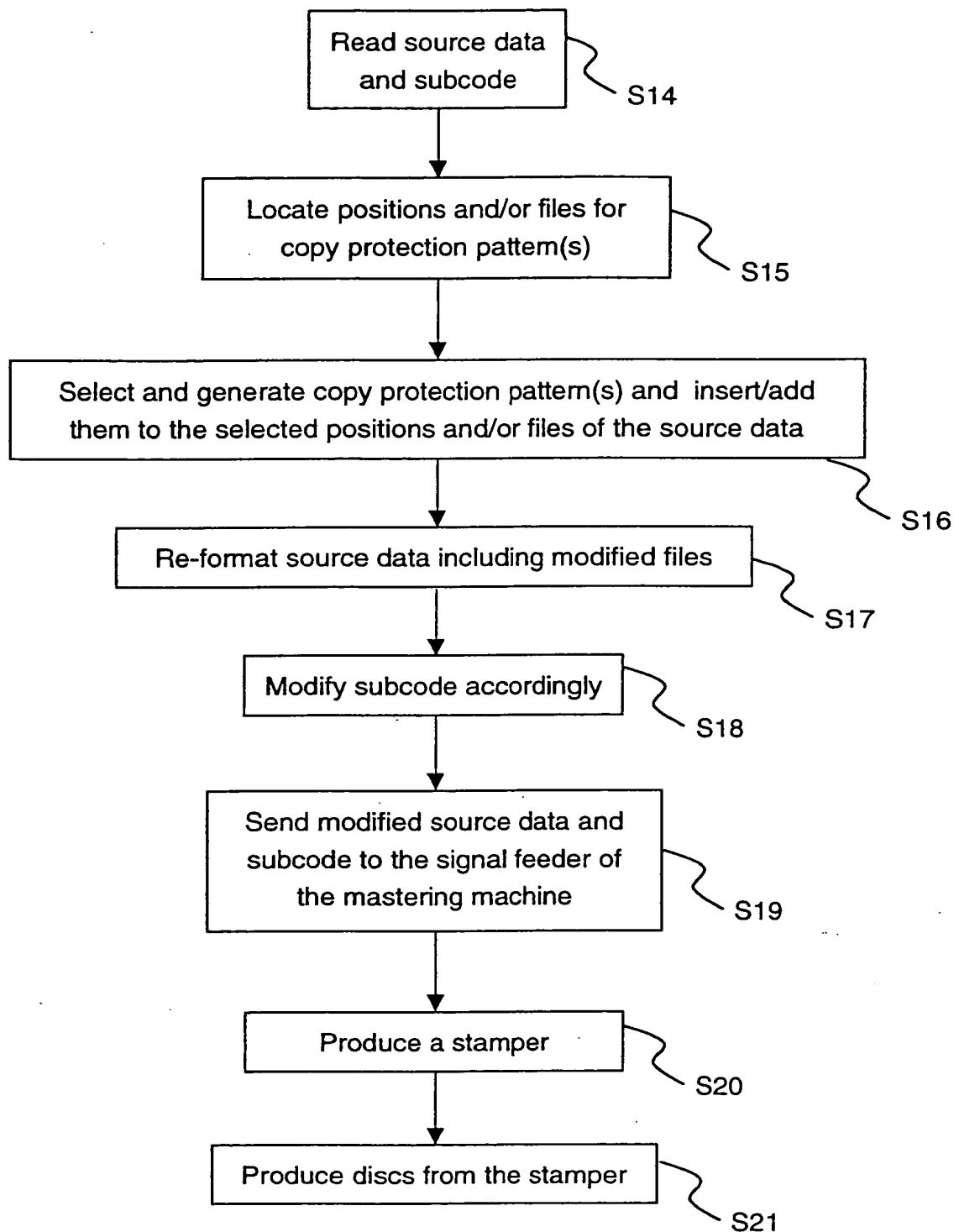
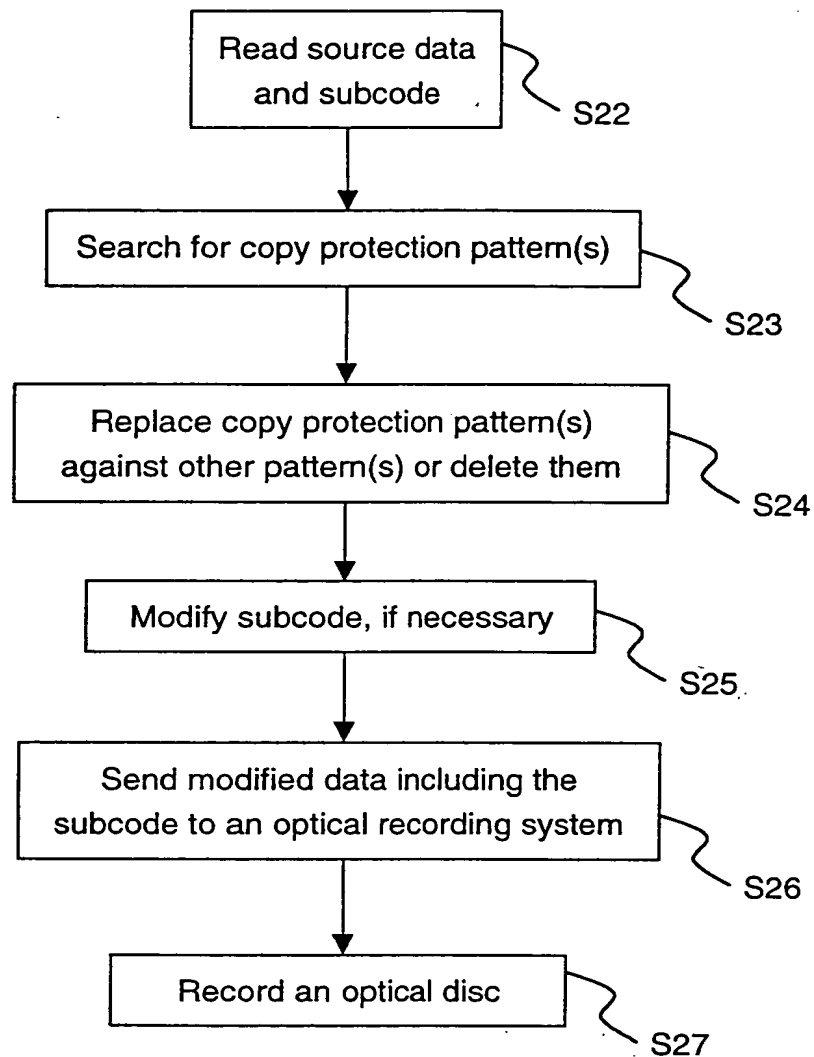


Fig. 4b

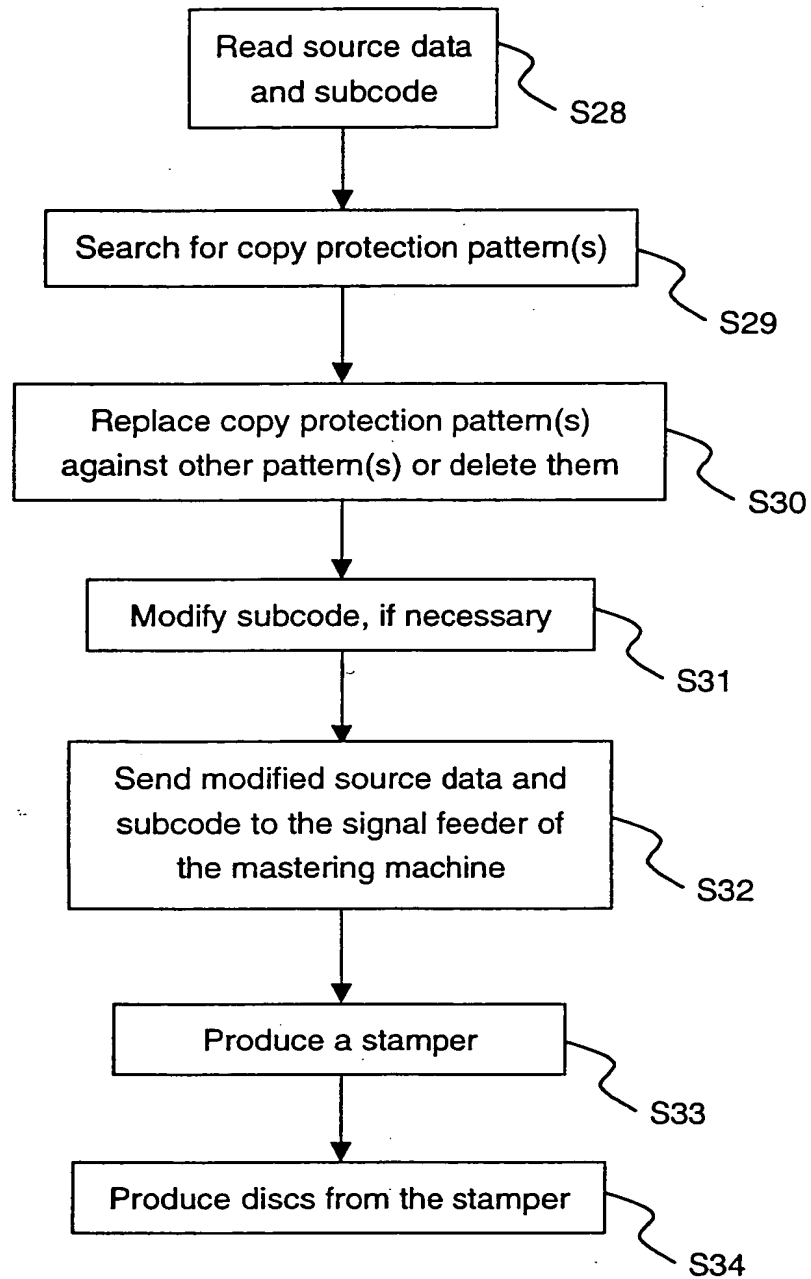
**Fig. 5**

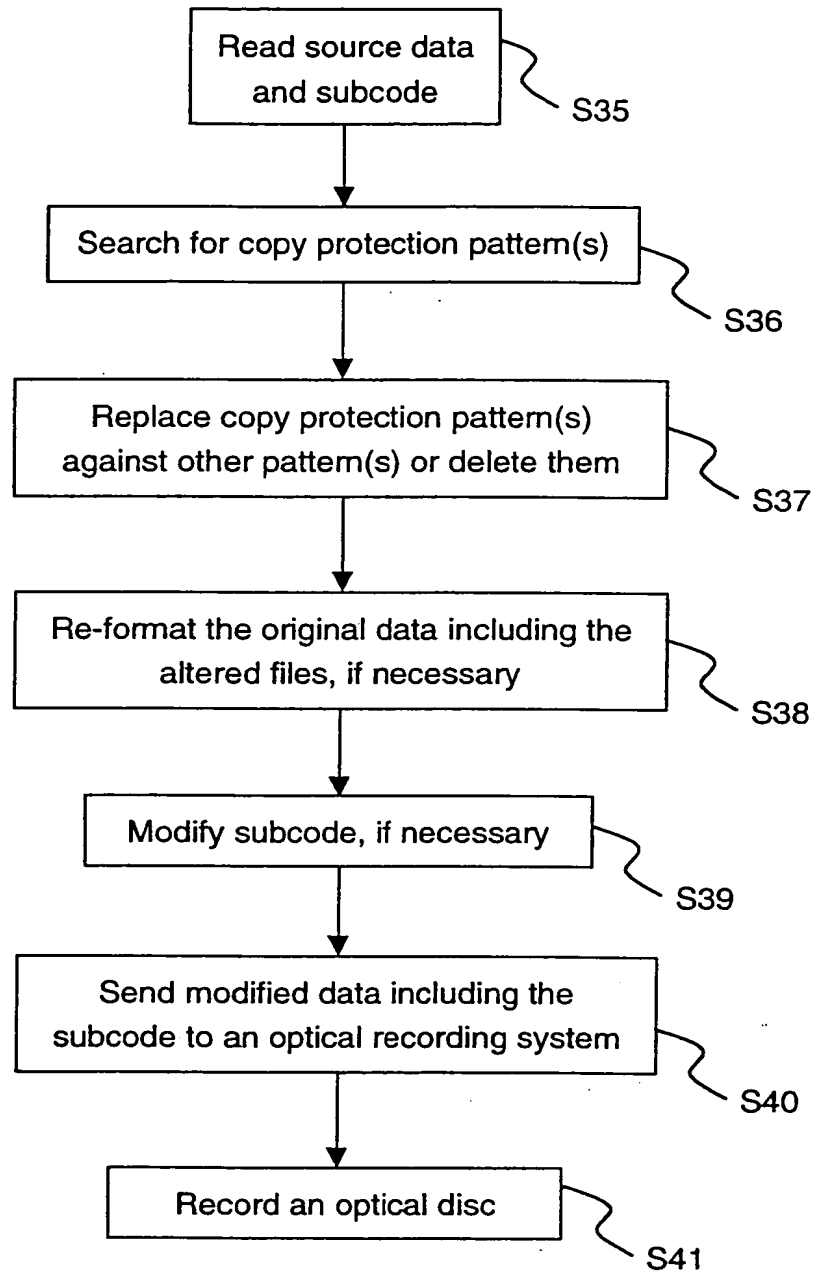
**Fig. 6**

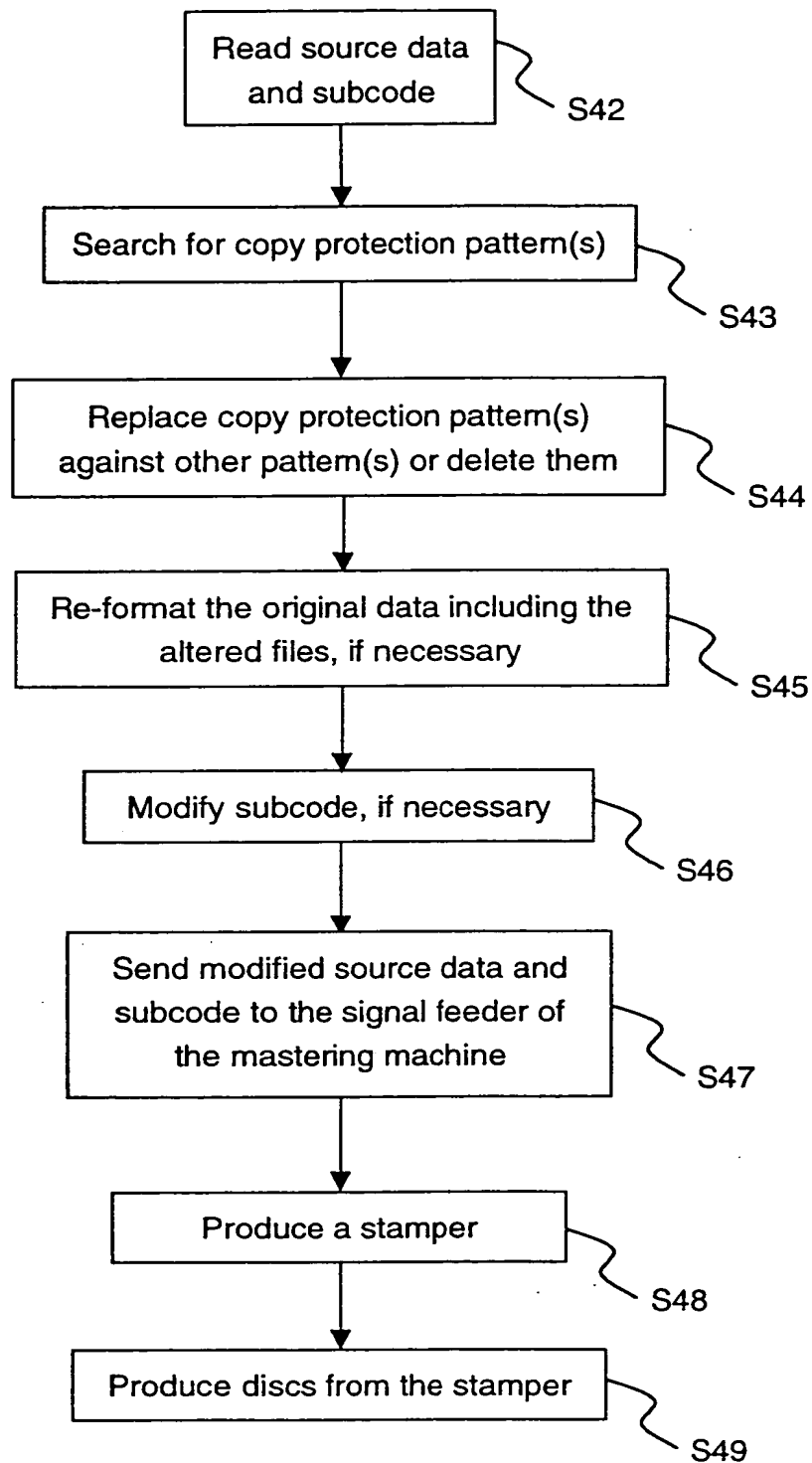
**Fig. 7**

**Fig. 8**



**Fig. 9**

**Fig. 10**

**Fig. 11**

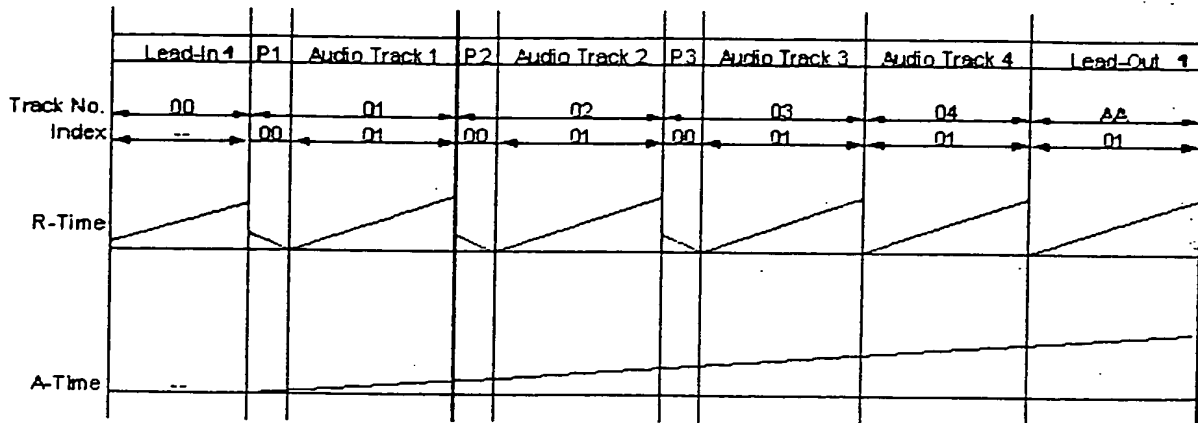


Fig. 12a

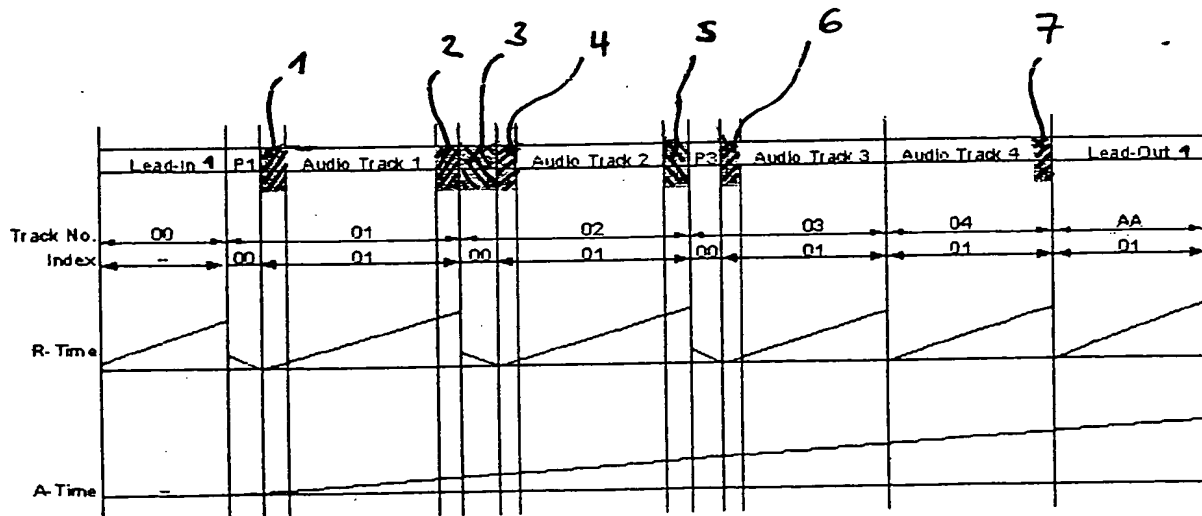


Fig. 12b

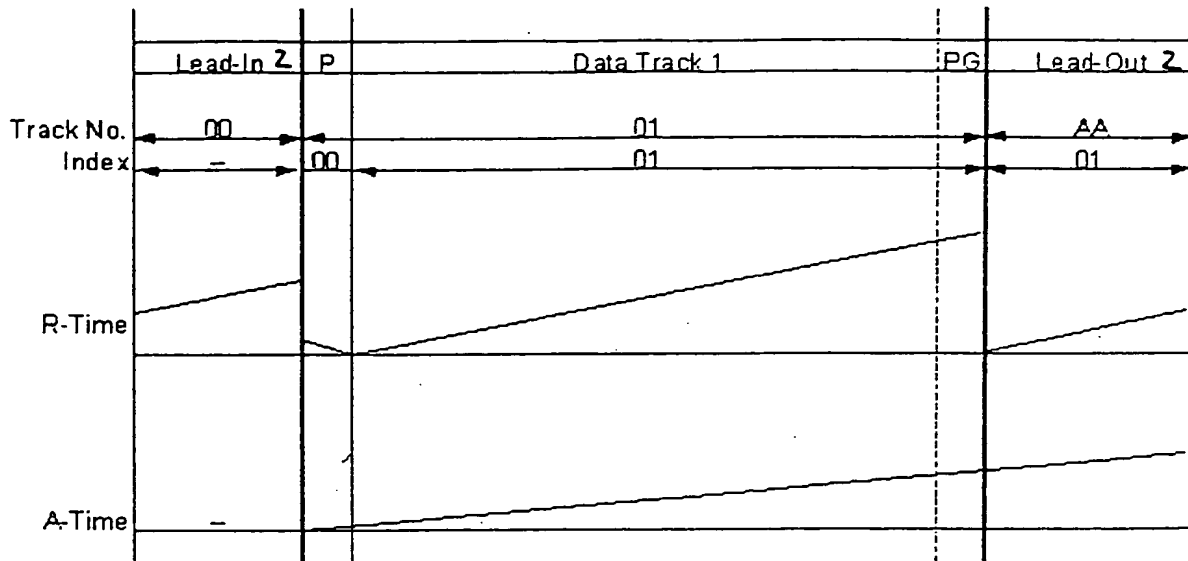


Fig. 13a

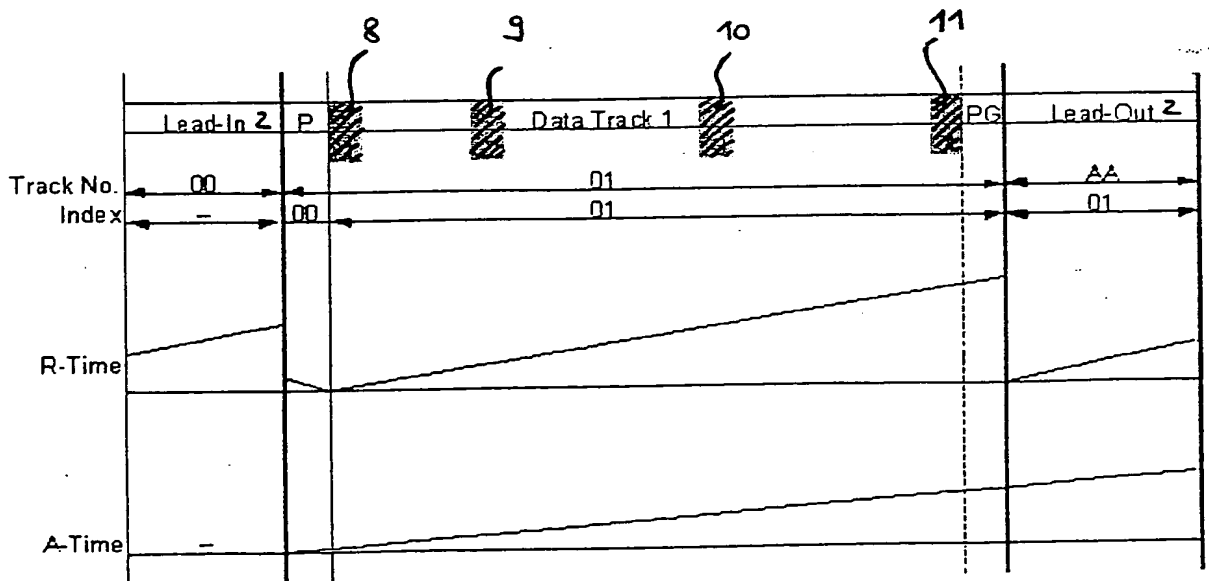


Fig. 13b